

Attorney Docket No. SEL 125

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:) I hereby certify that this correspondence
Shunpei YAMAZAKI et al	 is being deposited with the United States Postal Service as first class mail in an envelope addressed to:
Serial No.: 09/255,605) Commissioner for Patents, P.O. Box 1450,
Filed: February 22, 1999) Alexandria, VA 22313-1450, on <u>5/10/0</u> 6
Art Unit: 2673	Shannon Wallace
Examiner: Vincent E. Kovalick	
For: INFORMATION PROCESSING DEVICE) Date: May 10, 2006

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 C.F.R. §1.97, Applicant hereby calls the Examiner's attention to documents listed on the attached form, which documents may be material to the examination of this application. Copies of the references are submitted herewith for the Examiner's consideration.

No inference should be drawn that the attached list sets forth a comprehensive investigation of the prior art, that any or all are pertinent to the invention, or that any apparatus disclosed is equivalent to the subject invention.

The citation of the above-discussed documents is not to be construed as an assertion that more pertinent art could not possibly be in existence. Citation of any document herein is not

to be construed as an admission that any subject matter disclosed in the document is necessarily within the inventive field of endeavor, that any disclosure is necessarily prior in time to a particular date which may be relevant to the instant patent application, and/or that any disclosure is otherwise necessarily prior art with respect to the instant invention.

Applicant also respectfully reserves the right to later set forth how the instant invention is distinguished over the disclosure of any document or other art, including the disclosure of those documents discussed herein, that may be cited by the Examiner in rejecting a claim in the instant patent application.

A first office action, notice of allowance or issue fee notification has been received in this case, so a check in the amount of \$180.00 is enclosed. If any additional fee is required, please charge Deposit Account No. 50/1039.

Respectfully submitted,

Mark J. Murphy

Registration No: 34,225

COOK, ALEX, McFARRON, MANZO, CUMMINGS & MEHLER, LTD. 200 West Adams Street Suite 2850 Chicago, Illinois 60606 (312) 236-8500

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LIST OF PUBLICATIONS CITED BY APPLICANT		<u>Applicant</u> Shunpei YAMAZAKI et al					
		<u>Filing Date</u> February 22, 1999	<u>Group</u> 2673		_		
		US	PATENT DOCUMENTS				
*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS .	SUB- CLASS	FILING DATE	
FOREIGN PATENT DOCUMENTS							
	DOCUMENT NUMBER	DATE	APPLICANT	English Abstract	English Trans.	FILING DATE	
	EP 0 686 957 A2	12/13/95	Casio Computer			06/09/95	
	WO 96/07947	03/14/96	Corp Virtual I/O Inc			08/31/95	
	A1 EP 0 827 337 A1	03/04/98	Seiko Epson Corp			02/26/97	
				<u></u>			

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OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS

(Include name of author (in CAPITAL LETTERS), title of article or item (book, magazine, journal, serial, symposium, catalog, etc.) date, pages(s), volume-issue number(s), publisher, city and/or country where published).

- 1) SHINOHARA, T. et al, "High-Performance Polycrystalline Silicon TFTs Using Self-Aligned Grain Boundry Control Technique," Electronics and Communications in Japan, Part II: Electronics, vol. 76, no. 10, pp. 99-106, October, (1993).
- 2) KURIYAMA, H. et al, "Comprehensive Study of Lateral Grain Growth in Poly-Si Films by Excimer Laser Annealing and its Application to Thin Film Transistors," Japanese Journal of Applied Physics, vol. 33, part 1, no. 10, pp. 5657-5662, October (1994).
- 3) INUI, S. et al, "Thresholdless Antiferroelectricity in Liquid Crystals and its Application to Displays," J. Mater. Chem., vol. 6, no. 4, pp. 671-673, (1996).
- 4) KOBAYASHI, Y. et al, "The Application of a Bistable Device to Reflective Guest-Host LCDs," SID 97 Digest, pp. 405-408, (1997).
- 5) YOSHIDA, T. et al, "A Full-Color Thresholdless Antiferroelectric LCD Exhibiting Wide Viewing Angle with Fast Response Time," SID 97 Digest, pp. 841-844, (1997).
- 6) FURUE, H. et al, "Characteristics and Driving Scheme of Polymer-Stabilized Monostable FLCD Exhibiting Fast Response Time and High Contrast Ratio with Gray-Scale Capability," SID 98 Digest, pp. 782-785, (1998).
- 7) OHTANI, H. et al, "Late-News Poster: A 60-in. HDTV Rear-Projector with Continuous-Grain-Silicon Technology," SID 98 Digest, pp. 467-470 (1998).
- 8) OHTANI, H. et al, "Continuous-Grain Silicon Makes the HDTV Retro-Projector Sizzle," Information Display, vol. 14, no. 11, pp. 22-24, November, (1998).

EXAMINER:	
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DATE CONSIDERED:

*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP form. Draw line through citation if not in conformance and not considered. Include a copy of this form with the next communication to applicant.